PTO for autodialler prog.

4	2	6	1	Oct 84

PL602A

TP3 TP2 XTAL 1 4.433618 MHz TP5 TP4 TP1 TP6

+5v R18 2% 39K R19 2% 33K R20 2% 56K R21 2% 68K VR3 100K

IC1 TCM3101 OSC2 VDD OSC1 TXA CDL VSS RXB RXA RX1 RXD TXT1 CDT TXT2 TXD TPS CLK LK2

C1 220n R1 82K C3 220n C4 10n R6 12K

IC2 TL062 R22 82K R7 47K R9 120K

R2 560R R3 150K R5 100K LK1

T1 S P

RL1 10401 RL2 10101 RL3 10201

(RED) A (BLUE) B (GREEN) D (WHITE) C

R4 100R D1 D2 BZY75 C3V3 C5 4µ2 R8 470R C29 2µ2

R23 N.F. VR4 10K C30 100n R24 N.F. IC5 LM386 N/C

C8 47n C7 10µ 6V3 R12 10R Q R SPKR

IC3 N.F. TIL 124 (5KV) R10 10K R11 4K7 RING

IC4 CD4049 IC4 CD4049 IC6 CD4071 TX1200 TX75 SPKR

IC11 ULN2003

R16 470K C31 100n HOLD

IC12 CD4081 IC4 CD4049 IC6 CD4071

IC7 CD4020B IC9 CD4556B IC10 CD4520B LK3

SPKR IC6 CD4071 R13 1K

RL1 RL2 RL3

IC8 CD4081 DIAL DELAYED RTS

IC14 CD4071 IC11 ULN2003 R17 1K

IC8 CD4081 IC11 ULN2003A

GND (GREEN) RTS (WHITE) TXD (BLUE) RXD (RED) CTS (YELLOW)

FS1 FS63mA FS2 FS63mA FS3 FS63mA FS4 FS63mA R14 2K2 R15 2K2

D3 BZY96C5V1 D4 BZY96C5V1 D5 BZY96C5V1 D6 BZY96C5V1 D7 BZX70C12 D8 BZX70C12 D9 BZX70C12 D10 BZX70C12

IC13 DS96S1 C9 47p C10 47p

IC12 CD4081 IC14 CD4071 IC8 CD4081 SEIZE RING

DELAYED RTS TXD CDTENABLE

THESE COMPONENTS TO BE CONTAINED WITHIN ISOLATION BOX

BR1 1KAB10 T2 15v 0 0 15v 120 120

N L

C25 C26 47n C11 C20 47n C21 10µ 6V3 C22 C24 47n C25 10µ 6V3

FS5 FS250mA FS6 FS63mA

C32 0.1µ 10V C35 0.1µ 10V C33 220µ 63v C34 0.33µ 63v C36 220µ 50V C37 0.33µ 63v

IC15 LM341P5 IC16 LM320LZ5

TP7 TP8 TP9

ISSUE	A	B	C	D	1		2						TOLERANCE	TITLE	© COPYRIGHT 1983 ACORN COMPUTERS Ltd FULBOURN ROAD CHERRY HINTON CAMBRIDGE CB1 4JN
DATE	18 4 83	5 83		6-83	10 83	PPR 141	3 84	ECO 343					FINISH	PRESTEL ADAPTOR CIRCUIT DIAGRAM	
DRAWN	D B L	D B L	D.G.		D.G.		AMCE						MATERIAL	DRG No 115,000/C	ACORN COMPUTERS Ltd